

Abstract:

The objective is to develop a method for the detection and/or correction of memory access errors in a computer system, wherein test data (P) generated in addition to data (D) which is to be secured, using the latter data (D), is stored inside a memory (4) in such a fashion that a particularly high rate of reliability in error detection and correction can be achieved. To this end, in addition to the data (D) to be secured, the data's addresses are taken into account according to the invention when generating the test data (P).

Figure 1